

# SN54HC239, SN74HC239 DUAL 2-LINE TO 4-LINE DECODERS/DEMULTIPLEXERS

D2804, MARCH 1984—REVISED JUNE 1989

- Designed Specifically for High-Speed Memory Decoders and Data Transmission Systems
- Incorporates 2 Enable Inputs to Simplify Cascading and/or Data Reception
- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs
- Dependable Texas Instruments Quality and Reliability

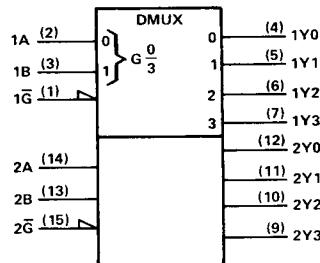
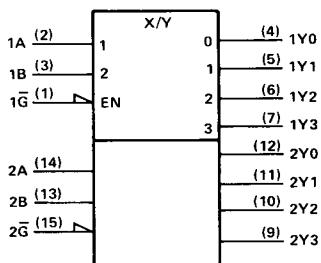
## description

The 'HC239 circuit is designed to be used in high-performance memory-decoding or data-routing applications requiring very short propagation delay times. In high-performance memory systems this decoder can be used to minimize the effects of system decoding. When employed with high-speed memories utilizing a fast enable circuit, the delay times of this decoder and the enable time of the memory are usually less than the typical access time of the memory. This means that the effective system delay introduced by the decoder is negligible.

The 'HC239 is comprised of two individual two-line to four-line decoders in a single package. The active-low enable input can be used as a data line in demultiplexing applications. These decoders/demultiplexers feature fully buffered inputs, each of which represents only one normalized load to its driving circuit.

The SN54HC239 is characterized for operation over the full military temperature range of  $-55^{\circ}\text{C}$  to  $125^{\circ}\text{C}$ . The SN74HC239 is characterized for operation from  $-40^{\circ}\text{C}$  to  $85^{\circ}\text{C}$ .

## logic symbols (alternatives)‡



<sup>‡</sup>These symbols are in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.  
Pin numbers shown are for D, J, and N packages.

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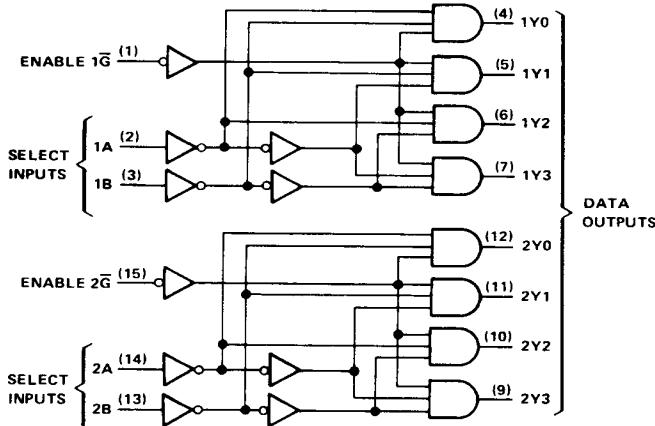
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HCMOS Devices

## logic diagram (positive logic)



Pin numbers shown are for D, J, and N packages.

FUNCTION TABLE

INPUTS		OUTPUTS			
ENABLE	SELECT	Y0	Y1	Y2	Y3
G	B A	L L L L	H L L L	L H L L	L L H L
H	X X	L L L L	H L L L	L H L L	L L H L
L	L L	L L L L	H L L L	L H L L	L L H L
L	L H	L H L L	L H L L	L L H L	L L H L
L	H L	L L L H	L L H L	L L H L	L L H L
L	H H	L L L H	L L H L	L L H L	L L H L

## absolute maximum ratings over operating free-air temperature range†

Supply voltage, V <sub>CC</sub> .....	-0.5 V to 7 V
Input clamp current, I <sub>IK</sub> (V <sub>I</sub> < 0 or V <sub>I</sub> > V <sub>CC</sub> ) .....	± 20 mA
Output clamp current, I <sub>OK</sub> (V <sub>O</sub> < 0 or V <sub>O</sub> > V <sub>CC</sub> ) .....	± 20 mA
Continuous output current, I <sub>O</sub> (V <sub>O</sub> = 0 to V <sub>CC</sub> ) .....	± 25 mA
Continuous current through V <sub>CC</sub> or GND pins .....	± 50 mA
Lead temperature 1.6 mm (1/16 in) from case for 60 s: FK or J package .....	300°C
Lead temperature 1.6 mm (1/16 in) from case for 10 s: D or N package .....	260°C
Storage temperature range .....	-65°C to 150°C

† Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

**SN54HC239, SN74HC239**  
**DUAL 2-LINE TO 4-LINE DECODERS/DEMULITPLEXERS**
**recommended operating conditions**

		SN54HC239			SN74HC239			UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	
V <sub>CC</sub>	Supply voltage	2	5	6	2	5	6	V
V <sub>IH</sub>	High-level input voltage	V <sub>CC</sub> = 2 V V <sub>CC</sub> = 4.5 V V <sub>CC</sub> = 6 V	1.5 3.15 4.2		1.5 3.15 4.2			V
V <sub>IL</sub>	Low-level input voltage	V <sub>CC</sub> = 2 V V <sub>CC</sub> = 4.5 V V <sub>CC</sub> = 6 V	0 0 0	0.3 0.9 1.2	0 0 0	0.3 0.9 1.2		V
V <sub>I</sub>	Input voltage		0	V <sub>CC</sub>	0	V <sub>CC</sub>		V
V <sub>O</sub>	Output voltage		0	V <sub>CC</sub>	0	V <sub>CC</sub>		V
t <sub>t</sub>	Input transition (rise and fall) times	V <sub>CC</sub> = 2 V V <sub>CC</sub> = 4.5 V V <sub>CC</sub> = 6 V	0 0 0	1000 500 400	0 0 0	1000 500 400		ns
T <sub>A</sub>	Operating free-air temperature		-55	125	-40	85		°C

**electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)**

PARAMETER	TEST CONDITIONS	V <sub>CC</sub>	T <sub>A</sub> = 25°C			SN54HC239		SN74HC239		UNIT
			MIN	TYP	MAX	MIN	MAX	MIN	MAX	
V <sub>OH</sub>	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> , I <sub>OH</sub> = -20 μA	2 V	1.9	1.998		1.9		1.9		V
		4.5 V	4.4	4.499		4.4		4.4		
		6 V	5.9	5.999		5.9		5.9		
	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> , I <sub>OH</sub> = -4 mA	4.5 V	3.98	4.30		3.7		3.84		
V <sub>OL</sub>	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> , I <sub>OL</sub> = -5.2 mA	6 V	5.48	5.80		5.2		5.34		V
		2 V	0.002	0.1		0.1		0.1		
		4.5 V	0.001	0.1		0.1		0.1		
		6 V	0.001	0.1		0.1		0.1		
I <sub>I</sub>	V <sub>I</sub> = V <sub>CC</sub> or 0	4.5 V	0.17	0.26		0.4		0.33		V
	V <sub>I</sub> = V <sub>CC</sub> or 0, I <sub>O</sub> = 0	6 V	0.15	0.26		0.4		0.33		
I <sub>CC</sub>	V <sub>I</sub> = V <sub>CC</sub> or 0, I <sub>O</sub> = 0	6 V		8		160		80	μA	
C <sub>i</sub>		2 to 6 V	3	10		10		10	pF	

**switching characteristics over recommended operating free-air temperature range (unless otherwise noted), C<sub>L</sub> = 50 pF (see Note 1)**

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V <sub>CC</sub>	T <sub>A</sub> = 25°C			SN54HC239		SN74HC239		UNIT
				MIN	TYP	MAX	MIN	MAX	MIN	MAX	
t <sub>pd</sub>	A or B	Y	2 V	62	150		225		190		ns
			4.5 V	18	30		45		38		
			6 V	14	26		38		32		
t <sub>pd</sub>	G̅	Y	2 V	53	120		180		150		ns
			4.5 V	14	24		36		30		
			6 V	11	20		31		26		
t <sub>pd</sub>		Y	2 V	38	75		110		95		ns
			4.5 V	8	15		22		19		
			6 V	6	13		19		16		

C <sub>pd</sub>	Power dissipation capacitance per decoder	No load, T <sub>A</sub> = 25°C	25 pF typ
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NOTE 1: Load circuit and voltage waveforms are shown in Section 1.